

P-Channel Enhancement Mode Power MOSFET

Description

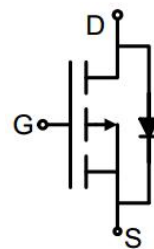
The G170P02D2 uses advanced trench technology to provide excellent $R_{DS(ON)}$, low gate charge. It can be used in a wide variety of applications.

General Features

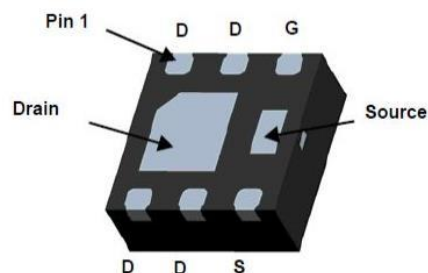
- V_{DS} -20V
- I_D (at $V_{GS} = -10V$) -16A
- $R_{DS(ON)}$ (at $V_{GS} = -4.5V$) < 17m Ω
- $R_{DS(ON)}$ (at $V_{GS} = -2.5V$) < 22m Ω
- 100% Avalanche Tested
- RoHS Compliant

Application

- Power switch
- DC/DC converters



Schematic diagram



DFN2x2-6L

Ordering Information

Device	Package	Marking	Packaging
G170P02D2	DFN2x2-6L	G170P02	3000pcs/Reel

Absolute Maximum Ratings $T_C = 25^\circ\text{C}$, unless otherwise noted

Parameter	Symbol	Value	Unit
Drain-Source Voltage	V_{DS}	-20	V
Continuous Drain Current	I_D	-16	A
Pulsed Drain Current (note1)	I_{DM}	-64	A
Gate-Source Voltage	V_{GS}	± 8	V
Power Dissipation	P_D	18	W
Operating Junction and Storage Temperature Range	T_J, T_{stg}	-55 To 150	$^\circ\text{C}$

Thermal Resistance

Parameter	Symbol	Value	Unit
Thermal Resistance, Junction-to-Ambient	R_{thJA}	6.9	$^\circ\text{C/W}$

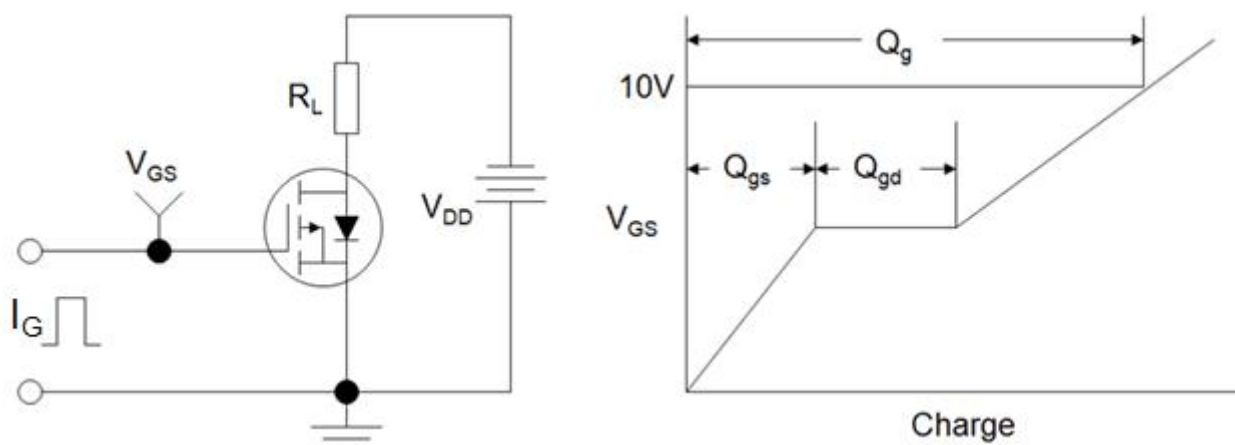
Specifications $T_J = 25^\circ\text{C}$, unless otherwise noted

Parameter	Symbol	Test Conditions	Value			Unit
			Min.	Typ.	Max.	
Static Parameters						
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	$V_{GS} = 0V, I_D = -250\mu A$	-20	--	--	V
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS} = -20V, V_{GS} = 0V$	--	--	-1	μA
Gate-Source Leakage	I_{GSS}	$V_{GS} = \pm 8V$	--	--	± 100	nA
Gate-Source Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = -250\mu A$	-0.5	-0.7	-1	V
Drain-Source On-Resistance	$R_{DS(on)}$	$V_{GS} = -4.5V, I_D = -6A$	--	13	17	m Ω
		$V_{GS} = -2.5V, I_D = -6A$	--	17	22	
Forward Transconductance	g_{FS}	$V_{DS} = -5V, I_D = -6A$	--	21	--	S
Dynamic Parameters						
Input Capacitance	C_{iss}	$V_{GS} = 0V,$ $V_{DS} = -10V,$ $f = 1.0MHz$	--	2179	--	pF
Output Capacitance	C_{oss}		--	278	--	
Reverse Transfer Capacitance	C_{rss}		--	276	--	
Total Gate Charge	Q_g	$V_{DD} = -10V,$ $I_D = -6A,$ $V_{GS} = -10V$	--	30	--	nC
Gate-Source Charge	Q_{gs}		--	5.3	--	
Gate-Drain Charge	Q_{gd}		--	7.6	--	
Turn-on Delay Time	$t_{d(on)}$	$V_{DD} = -10V,$ $I_D = -6A,$ $R_G = 2.5\Omega$	--	20	--	ns
Turn-on Rise Time	t_r		--	14	--	
Turn-off Delay Time	$t_{d(off)}$		--	95	--	
Turn-off Fall Time	t_f		--	65	--	
Drain-Source Body Diode Characteristics						
Continuous Body Diode Current	I_S	$T_C = 25^\circ C$	--	--	-16	A
Body Diode Voltage	V_{SD}	$T_J = 25^\circ C, I_{SD} = -6A, V_{GS} = 0V$	--	--	-1.2	V
Reverse Recovery Charge	Q_{rr}	$I_F = -6A, V_{GS} = 0V$ $di/dt = -100A/us$	--	34	--	nC
Reverse Recovery Time	T_{rr}		--	67	--	ns

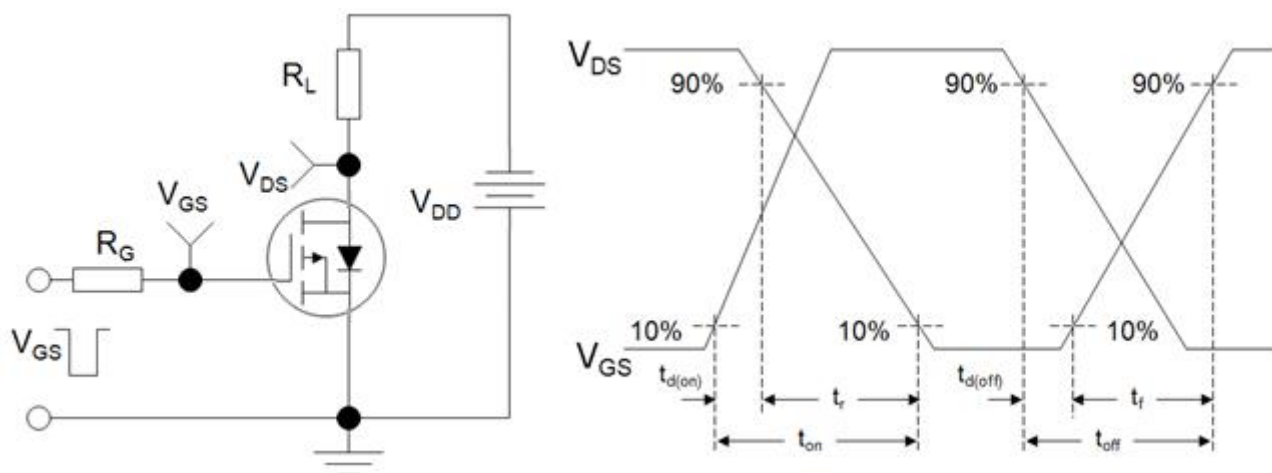
Notes

1. Repetitive Rating: Pulse width limited by maximum junction temperature
2. Identical low side and high side switch with identical R_G

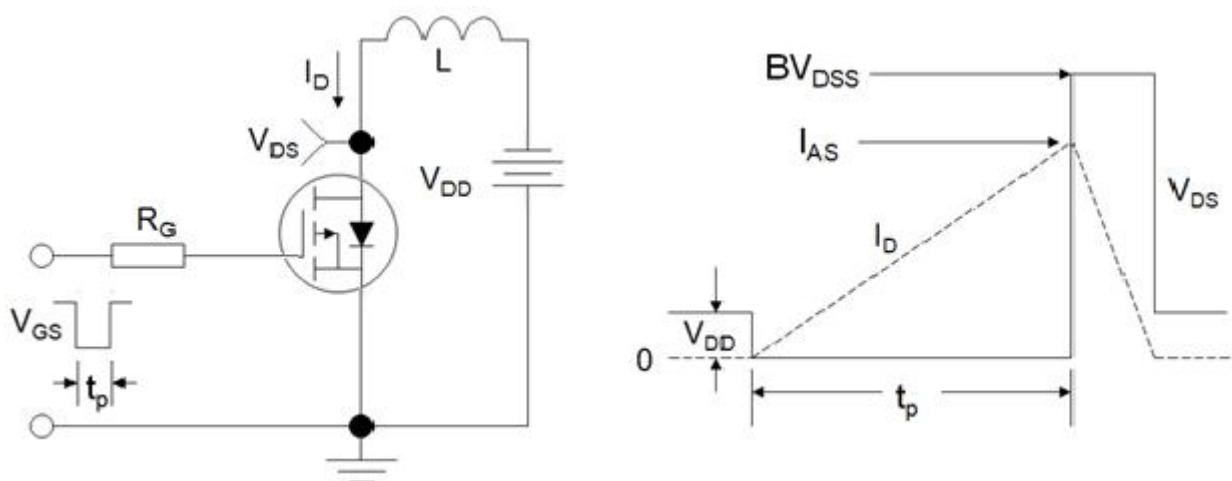
Gate Charge Test Circuit



Switch Time Test Circuit



EAS Test Circuit



Typical Characteristics $T_J = 25^\circ\text{C}$, unless otherwise noted

Figure 1. Output Characteristics

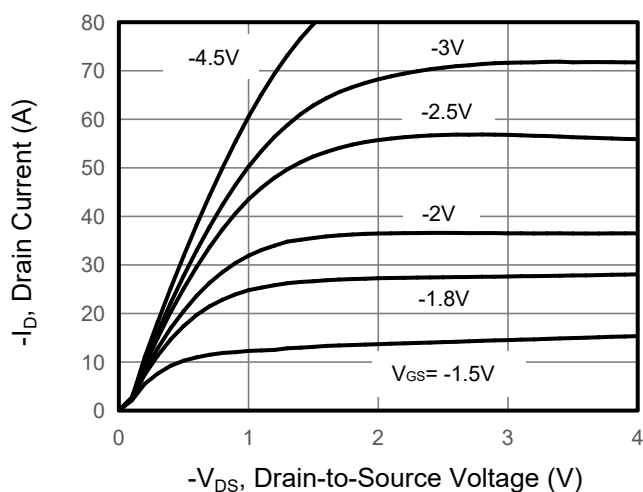


Figure 2. Transfer Characteristics

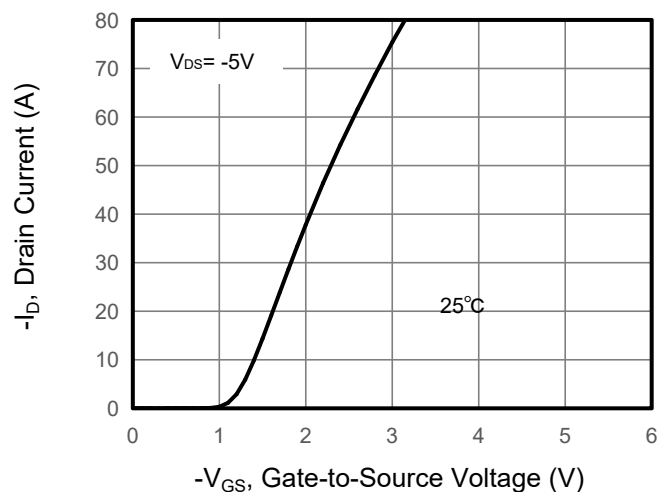


Figure 3. Drain Source On Resistance

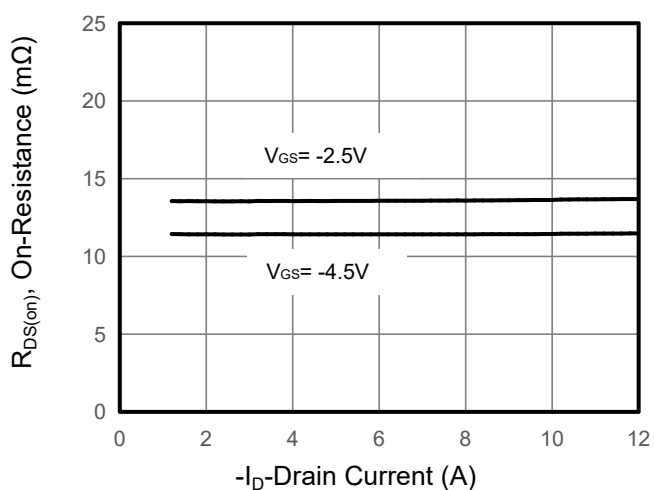


Figure 4. Gate Charge

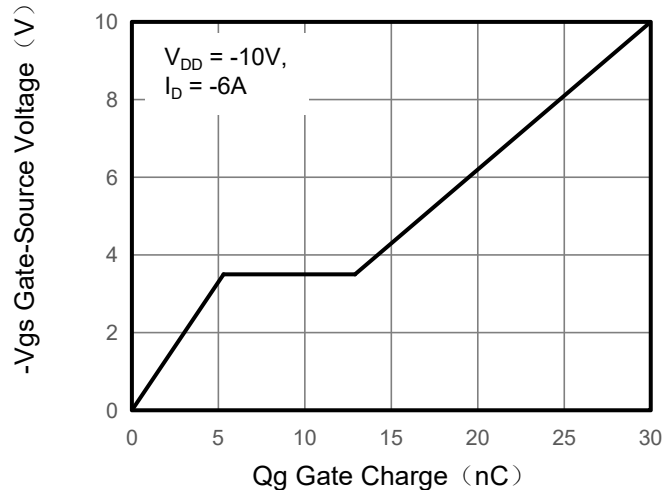


Figure 5. Capacitance

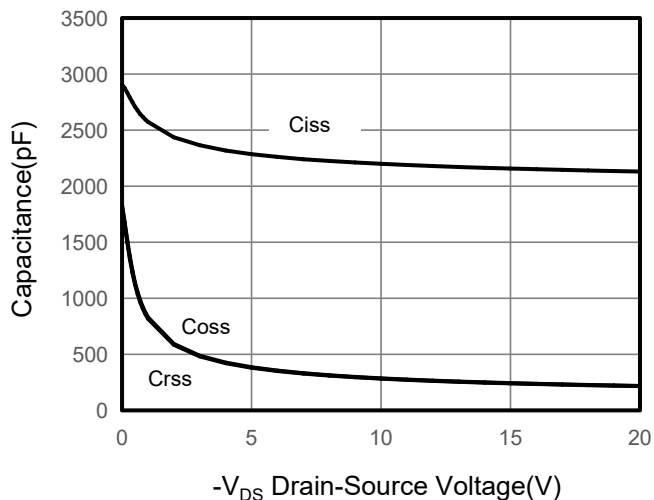
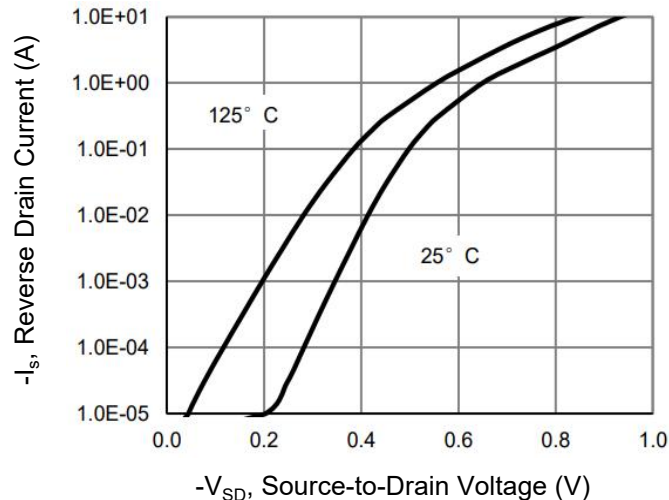


Figure 6. Source-Drain Diode Forward



Typical Characteristics $T_J = 25^\circ\text{C}$, unless otherwise noted

Figure 7. Drain-Source On-Resistance

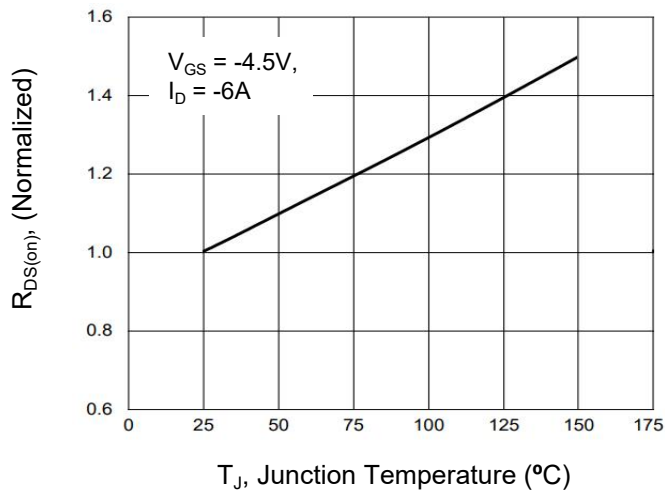


Figure 10. Safe Operation Area

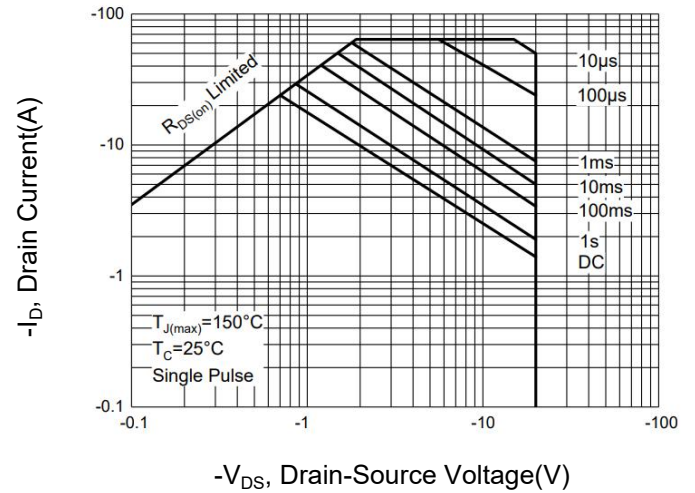
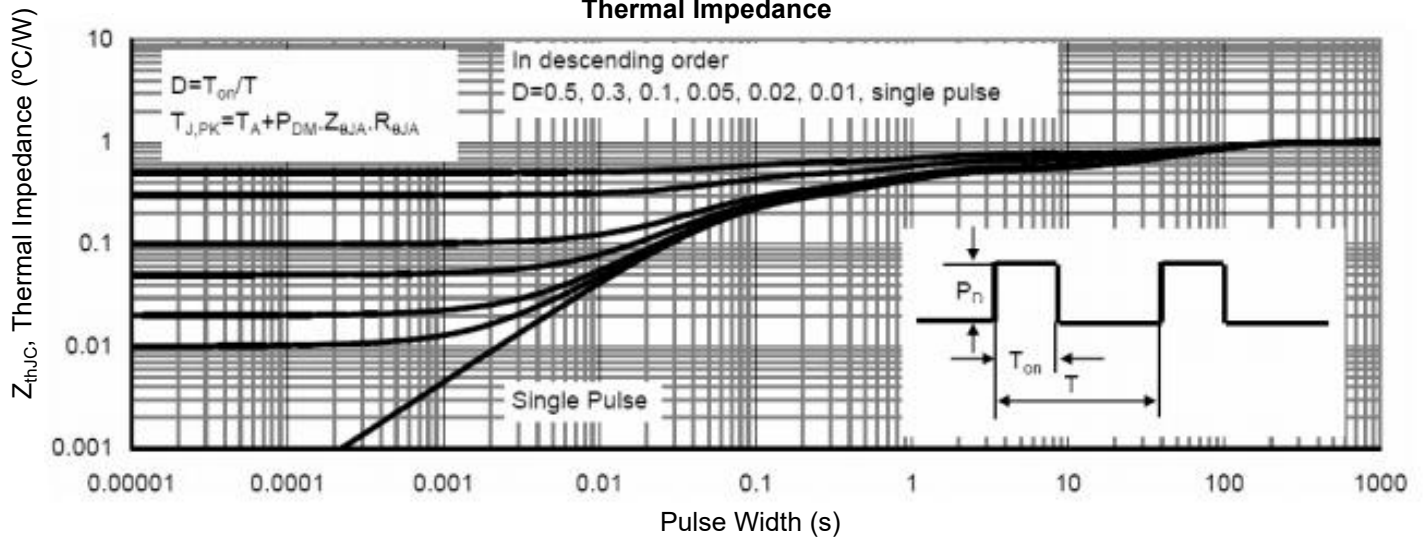
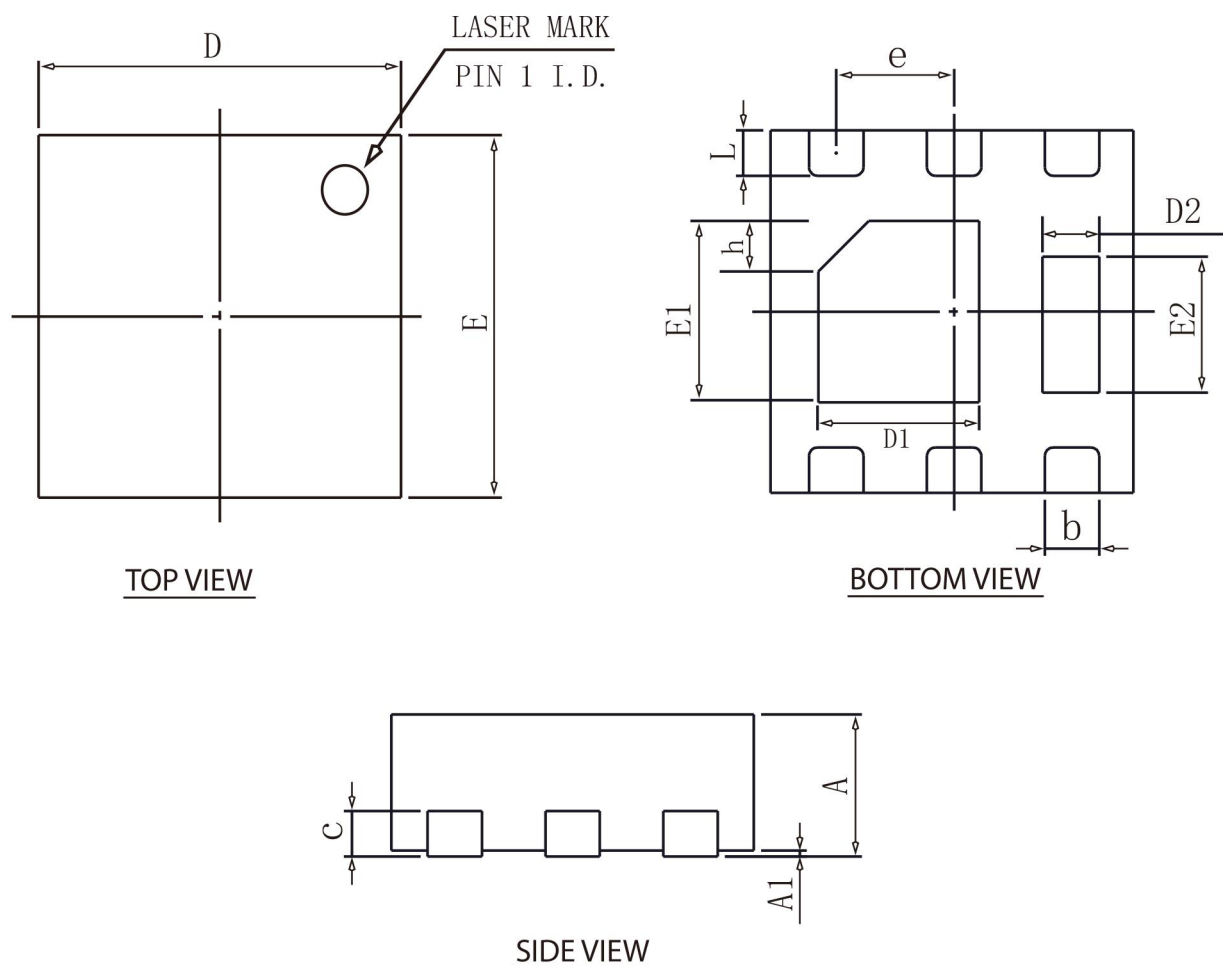


Figure 9. Normalized Maximum Transient Thermal Impedance



DFN2X2-6L Package Information



COMMON DIMENSIONS

SYMBOL	mm		
	MIN	NOM	MAX
A	0.45	0.50	0.55
A1	NA	0.02	0.05
b	0.20	0.27	0.34
c	0.18	0.20	0.25
D	1.95	2.00	2.07
E	1.95	2.00	2.07
D1	0.80	0.90	1.00
E1	0.90	1.00	1.10
D2	0.20	0.30	0.40
E2	0.65	0.75	0.85
L	0.20	0.25	0.35
h	0.20	0.25	0.30
e	0.65BSC		